Performance of LAPACK: A Portable Library of Numerical Linear Algebra Routines

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This paper describes the LAPACK project, an effort to produce a numerical linear algebra library that runs efficiently on shared memory vector and parallel processors. A description is given on what was done to achieve performance and results are given for various computers. In addition, future directions for research on parallel computers are discussed.

I. INTRODUCTION

The goal of the LAPACK project was to modernize the widely used LINPACK [7] and EISPACK [16], [14] numerical linear algebra libraries to make them run efficiently on shared memory vector and parallel processors. On these machines, LINPACK and EISPACK are inefficient because their memory access patterns disregard the multilayered memory hierarchies of the machines and spend too much time moving data instead of doing useful floating-point operations. LAPACK tries to cure this by reorganizing the algorithms to use a standardized set of block matrix operations known as the BLAS (Basic Linear Algebra Subprograms). These block operations can be optimized for each architecture to account for the memory hierarchy, and so provide a transportable way to achieve high efficiency on diverse modern machines.

We say "transportable" instead of "portable" because for fastest possible performance LAPACK requires that highly optimized block matrix operations be already implemented on each machine. Many computer vendors and researchers have developed optimized versions of the BLAS for specific environments, and we report some of their results in the context of LAPACK in this paper. Among other things, efficiency means that the performance (measured in millions of floating-point operations per second, or megaflops) should not degrade as the number of processors and the problem size increases; this property is frequently called scalability. For the subroutines in LAPACK, running time

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depends almost entirely on a problem's dimension alone, not just for algorithms with fixed operation counts like Gaussian elimination, but also for routines that iterate (to find eigenvalues, for example). Hence we can do performance tuning for the average case with some confidence that our optimizations will hold independent of the actual data.

Portability in its most inclusive sense means that the code is written in a standard language (say Fortran), and that the source code can be compiled on an arbitrary machine with an arbitrary Fortran compiler to produce a program that will run correctly and efficiently. We call this the "mail-order software" model of portability, since it reflects the model used by software servers such as netlib [11]. This notion of portability is quite demanding. It requires that all relevant properties of the computer's arithmetic and architecture be discovered at runtime within the confines of a Fortran code. For example, if the overflow threshold is important to know for scaling purposes, it must be discovered at runtime without overflowing, since overflow is generally fatal. Such demands have resulted in quite large and sophisticated programs [13] which must be modified continually to deal with new architectures and software releases. The mailorder software notion of portability also means that codes generally must be written for the worst possible machine expected to be used, thereby often degrading performance on all the others.

II. LAPACK OVERVIEW

Teams at the University of Tennessee, The University of California at Berkeley, the Courant Institute of Mathematical Sciences, the Numerical Algorithms Group, Ltd., Cray Research Inc., Rice University, Argonne National Laboratory, and Oak Ridge National Laboratory have developed a transportable linear algebra library called LAPACK (short for Linear Algebra Package) [1]. The library is intended to provide a coordinated set of subroutines to solve the most common linear algebra problems and to run efficiently on a wide range of high-performance computers.

LAPACK provides routines for solving systems of simultaneous linear equations, least squares solutions of linear systems of equations, eigenvalue problems, and singular value problems. The associated matrix factorizations (LU, Cholesky, QR, SVD, Schur, generalized Schur) are provided, as are related computations such as reordering the Schur factorizations and estimating condition numbers. Matrices may be dense or banded, but there is no provision for general sparse matrices. In all areas, similar functionality is provided for real and complex matrices, in both single and double precision. LAPACK is in the public domain and available from netlib.

The library is written in standard Fortran 77. The high performance is attained by doing most of the computation in the BLAS [9], [8], a standardized set of matrix-vector and matrix-matrix subroutines. Although Fortran implementations of the BLAS are provided with LAPACK, and many optimizing compilers can recognize some of the parallel constructs in these codes, consistent high performance can generally be attained only by using implementations optimized for a specific machine. In particular, most of the parallelism in LAPACK is embedded in the BLAS and is invisible to the user.

Besides depending upon locally implemented BLAS, good performance also requires knowledge of certain machine-dependent block sizes, which are the sizes of the submatrices processed by the Level 3 BLAS. For example, if the block size is 32 for the Gaussian Elimination routine on a particular machine, then the matrix will be processed in groups of 32 columns at a time. All of the tuning parameters in LAPACK are set via the integer function subprogram ILAENV, which can be modified for the local environment [2]. Details of the memory hierarchy determine the block size that optimizes performance.

III. PERFORMANCE TUNING

Performance tuning may not be of interest to users who wish to regard LAPACK as mail-order software. For those users, the Fortran BLAS, standard LAPACK, and the default blocking parameters in the auxiliary routine ILAENV are always an option. However, optimization of one or all three of these pieces may be necessary to achieve the best algorithm.

Thanks to strong support of the BLAS standard, the LAPACK approach of using the BLAS as building blocks has turned out to be a satisfactory mechanism for producing fast transportable code for dense linear algebra computations on shared memory machines. Gaussian elimination and its variants, QR decomposition, and the reductions to Hessenberg, tridiagonal, and bidiagonal forms for eigenvalue or singular value computations all admit efficient block implementations using Level 3 BLAS [4], [12]. Such codes are often nearly as fast as full assembly language implementations for sufficiently large matrices, although assembly language versions are typically better for small problems. Parallelism, embedded in the BLAS, is generally useful only on sufficiently large problems, and can, in fact, slow down processing on small problems. This means that the number of processors exercised should ideally be a

function of the problem size, something not always taken into account by existing BLAS implementations.

If a library of optimized BLAS exists and a LAPACK routine has been selected, the installer may wish to experiment with tuning parameters such as the block size. The most important issues affecting the choice of block size are

- A full set of optimized BLAS: Sometimes there is no advantage in using a blocked (Level 3 BLAS) algorithm over an unblocked (Level 2 BLAS) algorithm because some of the necessary BLAS have not been optimized.
- Level 3 BLAS versus Level 2 BLAS: On some machines, the memory bandwidth is high enough that the Level 2 BLAS are as efficient as the Level 3 BLAS, and choosing NB = 1 (i.e., using the unblocked algorithm) gives much better performance. This is particularly true for the block formulations of the QR factorization and reduction routines, since the block algorithm requires more operations than the unblocked algorithm, and the extra work is justified only if the Level 3 BLAS are faster than the Level 2 BLAS.
- Choice of block algorithm: Studies with different block algorithms for operations such as the LU factorization (DGETRF) often showed more dramatic differences than the choice of block size within the same algorithm. Details are given in the following section.
- Choice of unblocked algorithm: In LAPACK, the unblocked algorithm is always chosen to be the Level 2 BLAS equivalent of the higher level blocked algorithm, but a hybrid method (a block Crout LU factorization calling a right-looking algorithm within the block, for example) may give the best performance on a particular machine.

The choice of block size can have a significant effect on performance, but similar results are often observed for a range of block sizes. For example, on one processor of a CRAY Y-MP C90, the difference between the performance of a block algorithm for the worst block size is usually within about 10% of the performance with the best block size, and results within 5% of the best block size are common, so careful optimizations for each problem size may not be necessary.

Precise performance tuning is a difficult task. In principle, the optimal block sizes could depend on the machine configuration, problem dimensions, and user-controllable parameters such as the leading matrix dimension. In some environments, the machine configuration can change dynamically, further complicating this process. We used brute force during beta testing of LAPACK, running exhaustive tests on different machines, with ranges of block sizes and problem dimensions. This has produced a large volume of test results, too large for thorough human inspection and evaluation.

There appear to be at least three ways to choose block parameters. First, we could take the exhaustive tests we have done, find the optimal block sizes, and store them in tables in subroutine ILAENV; each machine would

Subroutine	Operation Count
_GEMV	2mn
_GEMM	2mkn
_GETRF	$mn^2 - 1/3n^3 - 1/2n^2$
_GETRI	$4/3n^3 - n^2$
_POTRF	$1/3n^3 + 1/2n^2$
_POTRI	$2/3n^3 + 1/2n^2$
_GEQRF	$2mn^2 - 2/3n^3 + mn + n^2$
ORGOR	$4mnk - 2(m+n)k^2 + 4/3k^3 +$
	$3nk - mk - k^2$
_GEHRD	$10/3n^3 - 1/2n^2$
SYTRD	$4/3n^3 + 3n^2$
_GEBRD	$4mn^2 - 4/3n^3 + 3n^2 - mn$

require its own special tables. Second, we could devise an automatic installation procedure which could run just a few benchmarks and automatically produce the necessary tables. Third, we could devise algorithms which tuned themselves at run-time, choosing parameters automatically [5], [6]. The choice of method depends on the degree of portability we desire.

IV. TIMING AND PERFORMANCE RESULTS

Over the course of the LAPACK project we have tested and tuned various algorithms and software on a number of different platforms [3], [4]. This was done with the help of test sites, including researchers from universities, research centers, and industry at over 50 locations in the United States, Canada, and 10 other countries. We are grateful to our friends and colleagues who have so generously contributed their time and computing resources to this project. We report some of their results in the tables that follow.

For all the performance results reported here, the software was run in full precision, 64-b arithmetic, for example, single precision on the Cray and double precision on the IBM.

The high-order terms of the operation counts used in computing the execution rates for the various routines are given below. Three parameters are used to count operations for SGEMM: the matrix dimensions m, n, and k. The

Table 1 Speed in Megaflops and Block Size of Best Variant, N = 500

Machine	DLUBL	DLUBC	DLUBR	NB
	(Left)	(Crout)	(Right)	
Fejitse VP2600	1238	1572	2036	64
CRAY Y-MP (8 proc)	1101	1261	1422	128
CRAY-2 (4 proc)	696	796	822	48
NEC SX2	423	577	495	1
Fujitsu VP-400 EX	344	398	526	64
CRAY-2 (1 proc)	340	361	331	64
CRAY Y-MP (1 proc)	278	284	286	16
Convex C240 (4 proc)	116	112	111	32
IBM 3090J	63	64	66	32
Alliant FX/80	35	52	53	32
IBM 3090-600E	53	50	56	35
FPS Model 500	20	47	33	1
HIM RISC/6000-530	32	34	34	48
Alliant FX/4	13	15	16	37

Table 2 LAPACK versus LINPACK, LU Factorization, N = 500

Machine	DGETRF	DGETF2	DGEFA	Speedup
	(BLAS 3)	(BLAS 2)	(BLAS 1)	
Fujitsu VP2600	2036	1199	229	8.9
CRAY Y-MP (8 proc)	1422	1400	173	8.2
CRAY-2 (4 proc)	822	584	93	8.8
NEC SX2	495	406	258	1.9
Pojitsu VP-400 EX	526	350	85	6.2
CRAY-2 (1 proc)	357	183	93	3.8
CRAY Y-MP (1 proc)	284	283	170	1.7
Convex C240 (4 proc)	111	68	14	7.9
IBM 3090J	66	25	24	2.8
Alliant FX/80	53	8	5.6	9.5
IBM 3090-600E	56	17	20	2.8
FPS Model 500	33	23	12	2.7
IBM RISC/6000-530	34	13	11	3.1
Alliant FX/4	16	4	3.2	4.9

Table 3 Speeds in Megaflops and the Ratio DGETRF/DGEMM, N = 500

Machine	DGEMM	DGEMV	DGETRF	Ratio
CRAY Y-MP C90 (8 proc)	7039	5229	3216	0.46
Pujitsu VP2600	4550	4315	2036	0.45
CRAY Y-MP (8 proc)	2449	2244	1422	0.58
CRAY-2 (4 proc)	1797	1276	822	0.46
NEC SX2	1272	1263	495	0.39
Pulitsu VP-400 EX	1080	1012	526	0.49
CRAY-2 (1 proc)	451	364	357	0.79
CRAY Y-MP (1 proc)	312	311	284	0.91
Convex C240 (4 proc)	159	150	111	0.70
IBM 3090J	107	74	66	0.62
Alliant FX/80	84	21	53	0.63
IBM 3090-600E	73	51	56	0.77
FPS Model 500	59	56	33	0.56
IBM RISC/6000-530	43	20	34	0.79
Alliant FX/4	19	11	16	0.80

Table 4 Speeds in Megaflops and the Ratio DGETRF/DGEMM, N=1000

Machine	DGEMM	DGEMV	DGETRF	Ratio
CRAY Y-MP C90 (8 proc)	7077	6757	4871	0.69
Fujitsu VP2600	4710	4539	3179	0.67
CRAY Y-MP (8 proc)	2448	2399	1926	0.79
CRAY 2 (4 proc)	1810	1386	1245	0.69
IBM RISC/6000-530	43	20	37	0.86

parameters used in counting operations for SGEMV and the LAPACK routines are the dimensions m and n of the $m \times n$ matrix.

Table 1 compares the performance of the block variants of the LU factorization for N = 500 on a number of different machines. We observe that the right-looking variant DLUBR gives the best performance in 10 of the 14 cases, and this was the variant finally chosen for the LAPACK routine DGETRF.

Table 2 compares the performance of the block LU factorization routine DGETRF (using Level 3 BLAS) with its best blocksize to the unblocked routine DGETF2 (using Level 2 BLAS) and to the LINPACK routine DGEFA, which uses only Level 1 BLAS. We also compute the speedup over LINPACK to show the actual improvement of LAPACK's DGETRF over DGEFA. The speedups range from around 2 on single processors of a CRAY Y-MP and NEC SX2 to 10 on a single processor of an Alliant FX/80. In particular, we see considerable improvements for the multiprocessors in this study.

Table 5 Speed in Megaflops, CRAY Y-MP C90, 8 Processors, Dedicated UNICOS 7.C, CFT77 5.0, libsci BLAS

				V	alues of	N			
Subroutine	100	250	500	750	1000	1250	1500	1750	2000
SGEMV('N',	842	2876	5936	8054	11142	11775	12154	12354	13087
SGEMM('N', 'N',	11185	13838	13000	14136	14075	14052	14102	14126	14183
SGETRF, M=N	454	1878	4406	6199	7799	8900	9695	10395	10919
SGETRI	1154	4613	8461	10293	11536	12054	12395	12754	13036
SPOTRF('U',	409	2103	5396	7841	9614	10456	11426	11921	1223
SPOTRF('L',	438	2227	5719	8113	9807	10764	11561	12021	1238
SPOTRIC'U',	489	2399	5549	7956	9695	10449	11593	12083	1252
SPOTRIC'L',	488	2413	5694	8025	9786	10863	11574	12155	1245
SSYTRF('U',	176	702	1865	3226	4424	5299	5988	6525	6956
SSYTRF('L',	172	698	1842	3223	4420	5311	5981	6516	6945
SGEQRF, M=N	400	1399	3770	4272	7571	8651	9528	10121	1051
SORGOR, M=N=K	546	1671	4279	4557	8063	9149	9984	10443	1093
SGERRD	541	1751	4569	6790	8550	9364	10262	10717	1110
SSYTRD('U',	302	1175	3128	4887	6384	7532	8425	9190	967
SSYTRD('L',	304	1192	3140	4874	6388	7554	8471	9147	972
SGEBRD	395	1369	3738	5793	7501	8571	9467	9978	1053

Table 6 Speed in Megaflops, Siemens/Fujitsu VP 2600-EX, 1 Processor Optimized BLAS from Universität Karlsrube

					Value	s of N				
Subcoutine '	100	200	300	400	500	600	700	800	900	1000
DGEMV('N',	1043	2316	3329	3839	4315	4217	4566	4416	4441	4589
DGEMM('N', 'N',	1567	2871	3545	4096	4550	4417	4743	4504	4554	4710
DGETRF, M=N	217	618	1123	1603	2036	2323	2667	2837	3010	3179
DGETRI	134	391	663	925	1167	1392	1605	1762	1928	2073
DPOTRF('U',	164	451	753	1030	1263	1451	1607	1725	1827	1966
DPOTRF('L',	148	351	627	901	1158	1398	1615	1797	1955	2108
DPOTRIC 'U',	129	332	562	791	1026	1232	1430	1609	1771	1918
DPOTRIC'L',	109	305	536	7-16	945	1128	1277	1.106	1509	1613
DSYTRF('U',	126	335	521	671	787	874	945	1001	1089	1174
DSYTRF('L',	123	303	485	632	751	841	917	978	1027	1100
DGEQRF, M=N	309	779	1165	1455	1653	1849	2075	2267	2404	255
DGEHRD	470	1094	1558	1859	2091	2197	2336	2449	2585	273
DSYTRD	116	268	413	517	668	777	876	962	1045	1129

† times reported for DLUBIC

Table 3 gives a measure of the efficiency of the LAPACK routine DGETRF at N=500. The efficiency is measured against the matrix multiply DGEMM from the Level 3 BLAS, which is generally the sustainable peak speed of these machines. We see that the speed of DGETRF is typically around 80% for the smaller machines, and an impressive 91% for one processor of a CRAY Y-MP, but the efficiency declines with multiprocessing. In part this is because N=500 is not a very big problem, so Table 4 extends the problem size to N=1000 for a few selected machines, with better results. For sufficiently large N, the speed of DGETRF and many other block algorithms should approach that of DGEMM.

We conclude this report by listing in Tables 5-20 the best megaflop rates for a selection of LAPACK routines on the computers in this study. We include data for the matrix factorizations DGETRF, DPOTRF, DSYTRF, and DGEQRF, the matrix inversion routines DGETRI and DPOTRI, the reduction routines DGEHRD, DSYTRD, and DGEBRD, and, if available, the orthogonal transformation routine DORGQR. All of these are blocked routines, and we use the best blocksize for each routine. This assumes that the routine to set the block size, ILAENV, will be optimized for each environment, and in fact a block size other than

Table 7 Speed in Megaflops, CRAY Y-MP C90, 1 Processor UNICOS 7.C, CFT77 5.0, libsci BLAS

					Values	of N			
Subcoutine	100	250	500	750	1000	1250	1500	1750	2000
SGEMV('N',	809	877	895	896	899	898	900	899	901
SCEMM('N', 'N',	862	893	898	899	900	900	900	901	901
SGETRF, M=N	359	669	796	837	858	867	874	879	882
SGETRI	554	789	858	876	883	887	890	892	893
SPOTRF('U',	285	621	784	834	854	865	873	877	881
SPOTRF('L'	289	623	785	833	854	866	873	878	882
SPOTRI('U',	330	664	811	850	866	874	880	883	886
SPOTRICT,	327	657	807	849	865	874	879	883	885
SSYTRF('U'	172	442	625	706	755	778	795	812	819
SSYTRF('L',	175	442	621	711	748	782	799	810	820
SGEQRE, M=N	352	699	788	818	831	836	838	842	845
SORGOR, M=N=K	516	757	814	828	835	840	814	844	848
SCEURD	512	764	821	835	843	848	851	851	848
SSYTRD('U',	290	588	747	798	824	841	849	858	864
SSYTRD('L',	295	599	753	799	826	842	852	858	861
SCEBRD	376	665	782	809	824	832	836	848	819

the five choices tested in the standard LAPACK timing suite may be the optimal one. The purpose here is not to benchmark the different computers, since most of these data were obtained under less than ideal conditions on a busy machine, but simply to demonstrate the performance of these block algorithms and, where the performance is low, identify areas for improvement in the BLAS or LAPACK routines. We specify "optimized BLAS" if anything

Table 8 Speed in Megaflops, NEC SX2-400, 1 Processor SXOR Ver. R4.11 FORTRAN77SX Rev. 041 Optimized BLAS from HNSX Supercomputers

		Va	dues of	N	
Subroutine	100	200	300	400	500
DGEMV('N',	706	1152	917	1202	1263
DGEMM('N', 'N',	784	1206	927	1216	1272
DGETRF, M=N	150	292	356	423	495
DGETRI	40	99	156	198	232
DPOTRF('U',	155	387	589	719	819
DPOTRF('L',	102	224	334	406	463
DPOTRI('U',	47	110	169	216	252
DPOTRI('L',	49	114	173	211	243
DSYTRF('U',	104	235	346	417	471
DSYTRF('L',	100	221	325	394	444
DGEQRF, M=N	217	498	617	690	768
DGEHRD	338	662	664	787	862
DSYTRD	117	244	335	392	430

Table 9 Speed in Megaflops, Siemens/Fujitsu VP 400-EX, 1 Processor Optimized BLAS from Universität Karlsruhe

	Values of N							
Subroutine	100	200	300	400	500			
DGEMV('N',	262	554	791	945	1012			
DGEMM('N', 'N',	327	633	882	994	1080			
DGETRF, M=N †	66	175	303	423	526			
DGETRI	40	109	183	255	324			
DPOTRF('U',	53	145	237	312	369			
DPOTRF('L',	49	136	222	302	376			
DPOTRI('U',	38	102	179	253	325			
DPOTRI('L',	32	90	155	210	264			
DSYTRF('U',	36	90	140	182	217			
DSYTRF('L',	32	82	130	171	206			
DGEQRF, M=N	101	237	329	388	457			
DGEHRD	141	307	411	481	517			
DSYTRD	37	89	138	183	223			

†- times reported for DLUBR

other than the Fortran BLAS are used, but in many cases only some of the BLAS have been optimized and further improvements in the BLAS could be made (and may have been made since these timings were obtained).

Several changes that became effective with the August 1991 test release of LAPACK do not appear in the older data from the April 1990 test release. The LU factorization subroutine DGETRF was changed to a right-looking variant after the August 1991 test release, so the data reported for DGETRF are generally taken from DLUBR (which is no longer provided with LAPACK). The LU inverse routine DGETRI was changed for the August 1991 release to a faster variant which does more of its work in the Level 3 BLAS routines DGEMM. Timings for the orthogonal transformation routine DORGQR and for the band reduction routine DGEBRD are available only in the August 1991 and later test releases. Also, an UPLO parameter was added to DSYTRD; results reported for DSYTRD with no indication of UPLO='U' or UPLO='L' are from the April 1990 version which assumed lower triangular storage.

Most of the data use the standard LAPACK data sets, which specifies matrices of order 100, 200, 300, 400, and 500, but in a few cases we have data for larger problems as well. For the CRAY Y-MP C90, some of the LAPACK

Table 10 Speed in Megaflops, CRAY-2, 1 Processor UNICOS 7.0, CFT77 5.0, libsci BLAS

100 289 415	200 324	300	400	500
		354	202	
415			353	365
	411	446	436	450
117	234	300	340	357
204	296	360	375	399
105	216	289	317	358
105	215	288	311	357
119	241	315	349	379
117	239	311	347	379
59	131	189	225	254
62	130	186	222	249
133	218	269	308	330
152	229	279	312	335
158	232	290	310	338
123	214	260	280	302
122	210	262	284	300
132	203	227	258	278
	105 105 119 117 59 62 133 152 158 123 122	105 216 105 215 119 241 117 239 59 131 62 130 133 218 152 229 158 232 123 214 122 210	105 216 289 105 215 288 119 241 315 117 239 311 59 131 189 62 130 186 133 218 269 152 229 279 158 232 290 123 214 260 122 210 262	105 216 289 317 105 215 288 311 119 241 315 349 117 239 311 347 59 131 189 225 62 130 186 222 133 218 269 308 152 229 279 312 158 232 290 310 123 214 260 280 122 210 262 284

Table 11 Speed in Megaflops, CRAY Y-MP, 6.41-ns Clock, 1 Processor UNICOS 7.0, CFT77 5.0, libsci BLAS

		Va	lues o	f N	
Subroutine	100	200	300	400	500
SGEMV('N',	260	268	287	285	291
SGEMM('N', 'N',	281	279	290	289	292
SGETRF, M=N	129	208	241	256	266
SGETRI	200	242	270	274	282
SPOTRF('U',	123	208	242	259	268
SPOTRF('L',	123	208	243	259	268
SPOTRI('U',	136	212	252	263	274
SPOTRI('L',	136	216	252	266	274
SSYTRF('U',	89	161	199	221	235
SSYTRF('L',	94	167	206	227	238
SGEQRF, M=N	162	234	256	266	272
SORGQR, M=N=K	194	250	265	271	276
SGEHRD	195	244	268	271	278
SSYTRD('U',	145	219	247	261	269
SSYTRD('L',	147	221	248	262	269
SGEBRD	161	234	256	266	272

†- times reported for Crout LU

Table 12 Speed in Megaflops, Convex C240, 4 Processors Optimized BLAS from Convex Computer Corporation

		Va	lues o	ſ N	
Subroutine	100	200	300	400	500
DGEMV('N',	44	70	102	128	150
DGEMM('N', 'N',	151	155	156	154	159
DGETRF, M=N 1	39	71	90	101	111
DPOTRF('U',	32	63	82	96	103
DPOTRF('L',	32	61	82	96	106
DSYTRF('U',	24	46	.56	- 66	76
DSYTRF('L',	23	45	53	66	76
DGEQRF, M=N	41	65	82	97	106
DGEHRD	45	61	77	89	108
DSYTRD	25	36	41	46	48

t- times reported for DLUBR.

routines are taken from CRAY's scientific library (libsci), but the block size was varied as in the other examples and the times for the best block size are shown.

V. FUTURE DIRECTIONS FOR RESEARCH

A new generation of even more massively parallel computers will soon emerge. Concurrent with the development of these more powerful parallel systems is a shift in the

Table 13 Speed in Megaflops, IBM 3090J-6VF, 1 Processor Optimized BLAS from ESSL

		Va	lues of	N	
Subroutine	100	200	300	400	500
DGEMV('N',	64	73	66	71	74
DGEMM('N', 'N',	87	102	103	103	107
DGETRF, M=N f	25	42	53	59	66
DGETRI	16	24	28	30	32
DPOTRF('U',	30	44	56	62	67
DPOTRF('L',	48	63	65	66	67
DPOTRI('U',	14	22	25	27	28
DPOTRI('L',	13	20	26	29	31
DSYTRF('U',	37	55	65	71	75
DSYTRF('L',	37	55	64	71	75
DGEQRF, M=N	34	60	71	75	80
DGEHRD	37	59	65	70	74
DSYTED	19	28	32	35	37

†- times reported for DLUBR

Table 14 Speed in Megaflops, IBM 3090-600 E/VF, 1 Processor VM/XA SP, VS Fortran Ver. 2.4, Optimized BLAS from ESSL Rel. 4, Umeå University, NAG MARK 13, and IBM ECSEC

		Va	ues of	N	
Subcoutine	100	200	300	400	500
DGEMV('N',	44	45	46	48	51
DGEMM('N', 'N',	70	70	70	71	73
DGETRF, M=N †	28	39	47	52	.55
DGETRI	18	32	42	47	-52
DPOTRF('U',	20	35	42	47	49
DPOTRF('L',	22	36	44	48	51
DPOTRI('U',	18	34	42	48	53
DPOTRI('L',	16	31	40	46	50
DSYTRF('U',	22	33	38	42	45
DSYTRF('L',	22	32	39	42	45
DGEQRF, M=N	27	40	46	51	54
DGEHRD	27	39	43	48	52
DSYTED	15	24	29	32	34

Table 15 Speed in Megaflops, IBM RISC/6000-550

t- times reported for DLUBR

		Va	lues of	f N	
Subroutine	100	200	300	400	500
DGEMV('N',	29	29	32	30	33
DGEMM('N', 'N',	67	70	70	71	72
DGETRF, M=N	22	33	39	44	47
DGETRI	44	56	63	64	66
DPOTRF('U',	34	54	65	65	67
DPOTRF('L',	34	54	56	61	65
DPOTRI('U',	34	45	58	59	63
DPOTRI('L',	34	54	62	63	65
DSYTRF('U',	17	22	27	34	34
DSYTRF('L',	17	27	29	36	36
DGEQRF, M=N	27	43	45	49	53
DORGQR, M=N=K	27	41	46	51	53
DGEHRD	24	36	41	43	45
DSYTRD('U',	27	28	29	29	30
DSYTRD('L',	27	28	28	29	30
DGEBRD	22	24	27	30	32

computing practices of many scientists and researchers. Increasingly, the tendency is to use a variety of distributed computing resources, with each individual task assigned to the most appropriate architecture, rather than to use a single, monolithic machine. The pace of these two developments, the emergence of highly parallel machines, and the move to a more distributed computing environment have been so rapid that software developers have been unable to keep up. Part of the problem has been that

Table 16 Speed in Megaflops, IBM RISC/6000-530 AIX 3.1, XL FORTRAN, Optimized BLAS from IBM ECSEC

					Value	es of N				
Subcoutine	100	200	300	400	500	600	700	800	900	1000
DGEMV('N',	18	19	19	20	20	20	20	20	20	20
DGEMM('N', 'N',	42	44	44	43	43	43	43	43	44	43
DGETRF, M=N 1	19	26	29	32	34	34	35	36	37	37
DGETRI	22	31	35	36	38	38	39	39	40	40
DPOTRF('U',	24	29	34	36	38	39	40	40	41	41
DPOTRF('L',	19	25	29	32	34	34	35	36	37	37
DPOTRI('U',	17	23	26	27	27	27	28	28	27	28
DPOTRI('L',	17	22	25	25	25	25	26	26	26	26
DSYTRF('U',	15	20	23	25	27	29	29	29	30	31
DSYTRF('L'	13	19	23	24	26	28	28	29	30	30
DGEQRF, M=N	19	26	30	32	34	34	35	36	36	37
DGEHRD	16	21	22	23	24	24	25	25	26	26
DSYTRD	15	16	17	17	17	18	18	18	18	18

†- times reported for DLUBR

Table 17 Speed in Megaflops, Alliant FX/80, 8 Processors

		Va	lues of	N	
Subroutine	100	200	300	400	500
DGEMV('N',	24	30	22	21	21
DGEMM('N', 'N',	74	76	78	78	84
DGETRF, M=N †	13	30	41	47	53
DGETRI	8	20	28	36	42
DPOTRF('U',	11	27	38	49	54
DPOTRF('L',	10	20	25	27	29
DPOTRI('U',	- 6	- 11	14	15	1.5
DPOTRI('L',	7	15	20	23	26
DSYTRF('U',	9	15	22	29	32
DSYTRF('L',	8	15	23	29	33
DGEQRF, M=N	11	28	39	47	50
DGEHRD	13	20	25	27	23
DSYTRD .	13	18	18	19	1.8

†- times reported for DLUBR

Table 18 Speed in Megaflops, FPS Model 500, 1 Processor Optimized BLAS from FPS Computing

		Va	loes of	í N	
Subroutine	100	200	300	400	500
DGEMV('N',	39	48	52	54	56
DGEMM('N', 'N',	37	50	56	57	59
DGETRF, M=N	13	19	25	29	33
DGETRI	18	32	42	47	52
DPOTRF('U',	20	35	42	47	49
DPOTRF('L',	22	36	44	48	51
DPOTRI('U',	18	34	42	48	53
DPOTRI('L',	16	31	40	46	50
DSYTRF('U',	22	33	38	42	45
DSYTRF('L',	22	32	39	42	45
DGEQRF, M=N	27	40	46	-51	54
DGEHRD	27	39	43	48	52
DSYTRD	15	24	29	32	34

†- times reported for DLUBR

supporting system software has inhibited this development. Consequently, exploiting the power of these technological advances has become more and more difficult. Much of the existing reusable scientific software, such as that found in commercial libraries and in public domain packages, is no longer adequate for the new architectures. If the full power of these new machines is to be realized, then scalable libraries, comparable in scope and quality to those that currently exist, must be developed.

One of our goals as software designers is to communicate to the high-performance computing community algorithms and methods for the solution of system of linear equations.

Table 19 Speed in Megaflops, Alliant FX/4, 4 Processors BLAS from FX/Series Linear Algebra Library, Ver. 5

		Va	lues o	í N	
Subroutine	100	200	300	400	500
DGEMV('N',	11	12	11	11	11
DGEMM('N', 'N',	- 17	18	19	19	19
DGETRF, M=N 1	7	11	13	15	16
DGETRI	- 5	9	11	13	14
DPOTRF('U',	- 5	11	13	14	15
DPOTRF('L',	5	10	11	11	- 11
DPOTRI('U',	4	- 6	7	7	7
DPOTRI('L',	- 4	7	- 8	9	10
DSYTRF('U',	4	- 8	10	12	13
DSYTRF('L',	4	8	10	12	13
DGEQRF, M=N	6	-11	13	14	14
DGERRD	7	- 8	10	10	11
DSYTRD	6	- 8	8	7	7

† times reported for DLUBR

Table 20 Speed in Megaflops, Stardent 3000 3.01 FCS Fortran BLAS-O2-Inline (Vectorization Only)

	100	Va	lucs of	N N	
Subroutine	100	200	300	400	500
DGEMV('N',	10	12	13	1.3	12
DGEMM['N', 'N',	1.0	- 11	1.1	.11	12
DGETRF, M=N †	7	10	-11	12	12
DGETRI	7	9	8	10	- 11
DPOTRF('U',	7	9	11	13	13
DPOTRF('L',	6	9	10	11	11
DPOTRICTU	- 6	8	10	-11	11
DPOTRI('L',	- 6	9	10	11	- 11
DSYTRF('U',	- 8	10	- 11	11	12
DSYTRF('L',	7	10	- 11	12	13
DGEQRF, M=N	10	12	13	14	1-
DGERRD	10	12	13	13	1-
DSYTRD	- 8	9	10	11	11

[†] times reported for DLUBR

In the past we have provided black-box software in the form of a mathematical software library, such as LAPACK, LIN-PACK, NAG, and IMSL. These software libraries provide for:

- · easy interface with hidden details
- · reliability; the code should fail as rarely as possible
- · speed.

The high-performance computing community, on the other hand, which wants to solve the largest, hardest problems as quickly as possible, seems to want

- · speed
- access to internal details to tune data structures to the application
- algorithms which are fast for the particular application even if not reliable as general methods.

These differing priorities make for different approaches to algorithms and software. The first set of priorities leads us to produce "black boxes" for general problem classes. The second set of priorities seems to lead us to produce "template codes" or "toolboxes" where the users can assemble, modify, and tune building blocks starting from well-documented subparts. This leads to software which is not going to be reliable on all problems, and requires extensive user tuning to make it work. This is just what the block-box users do not want.

In scientific high-performance computing we see three different computational platforms emerging, each with a distinct set of users. The first group of computers contains the traditional supercomputer. Computers in this group exploit vector and modest parallel computing. They are general-purpose computers that can accommodate a large cross section of applications while providing a high percentage of their peak computing rate. They are the computers typified by the Cray Y-MP C90, IBM ES/9000, and NEC SX-3; the so- called general-purpose vector supercomputers.

The second group of computers are the highly parallel computers. These machines often contain hundreds or even thousands of processors, usually RISC in design. The machines are usually loosely coupled having a switching network and relatively long communication times compared to the computational times. These computers are suitable for fine-grain and coarse-grain parallelism. As a system, the cost is usually less than the traditional supercomputer and the programming environment is very poor and primitive. There is no portability since users' programs depend heavily on a particular architecture and on a particular software environment.

The third group of computers are the clusters of workstations. Each workstation usually contains a single very fast RISC processor. Each workstation is connected through a Local Area Network, or LAN, and as such the communication time is very slow, making this setup not very suitable for fine-grain parallelism. They usually have a rich software environment and operating system on a workstation node, usually UNIX. This solution is usually viewed as a very cost-effective solution compared to the vector supercomputers and highly parallel computers.

Users are in general not a monolithic entity, but in fact represent a wide diversity of needs. Some are the sophisticated computational scientists who eagerly move to the newest architecture in search of ever-higher performance. Others want only to solve their problems with the least change to their computational approach.

We hope to satisfy the high-performance computing community's needs by the use of reusable software templates. With the templates we describe the basic features of the algorithms. These templates offer the opportunity for whatever degree of customization the user may desire, and also serve a valuable pedagogical role in teaching parallel programming and installing a better understanding of the algorithms employed and results obtained. While providing the reusable software templates we hope to retain the delicate numerical details in many algorithms.

We believe it is important for users to have trust in the algorithms, and hope this approach conveys the spirit of the algorithm and provides a clear path for implementation where the appropriate data structures can be integrated into the implementation. We believe that this approach of templates allows for easy modification to suit various needs. More specifically, each template should have:

 Working software for as general a matrix as the method allows.

- Mathematical description of the flow of the iteration.
- 3) Algorithms described in a Fortran-77 program with calls to BLAS [15], [9], [8] and LAPACK routines
- Discussion of convergence and stopping criteria.
- Suggestions for extending a method to more specific matrix types (for example, banded systems).
- 6) Suggestions for tuning (for example, which preconditioners are applicable and which are not).
- Performance: when to use a method and why.
- 8) Reliability: for what class of problems the method is appropriate.
- 9) Accuracy: suggestions for measuring the accuracy of the solution or the stability of the method.

An area where this will work well is with sparse matrix computations. Many important practical problems give rise to large sparse systems of linear equations. One reason for the great interest in sparse linear equations solvers and iterative methods is the importance of being able to obtain numerical solutions to partial differential equations. Such systems appear in studies of electrical networks, economicsystem models, and physical processes such as diffusion, radiation, and elasticity. Iterative methods work by continually refining an initial approximate solution so that it becomes closer and closer to the correct solution. With an iterative method a sequence of approximate solutions $\{x^{(k)}\}$ is constructed which essentially involve the matrix A only in the context of matrix-vector multiplication. Thus the sparsity can be taken advantage of so that each iteration requires O(n) operations.

Many basic methods exist for iteratively solving linear systems and finding eigenvalues. The trick is finding the most effective method for the problem at hand. The method that works well for one problem type may not work as well for another. Or it may not work at all.

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