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(d, k) [Ake65c, Fri66e, Kor67c]. (d, k, μ) [Qua69]. 1 [Wei69b]. 15 [Sei64]. 2 [EKP67, Fia66, Fre67a, Roy68b, SA64, Sch69b, Wei69b]. 21/2 [Gil66d]. 2×1 [Set66a]. 3 [AS66, Fre67a, Kei64, SA64, Wol69]. 4 [Fre67a]. 48 [Eps68]. $[n, k]$ [Lec64]. $_2$ [MS66]. $_3$ [MS66]. a [Mul59d, Mul60]. $\cos x$ [Spe65]. $\cot^{-1} x$ [Spe65]. $\exp x$ [Spe65]. F [Sed68, Hsi68]. γ [MS66]. k [Lem69, Liu63b, Liu64a]. $k = 4$ [Bra66]. $\ln x$ [Spe65]. m [Fro65]. N [Ber58, Ble61b, Dav65, Hig61b, MN61, BB62, Col69a, Fro65, Lee62a, Ull69b]. P^n [Mow66]. π [GH59]. r [HS69c]. $\Sigma(k)$ [Bra66]. $\sin x$ [Spe65]. $t*$ [Tay65]. $\tan^{-1} x$ [Spe65]. θ [Bro60b]. X [Ort59]. x^y [Kri64]. Z [Tou60, Fuk64].

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[Azg68] V. Azgapetian. Comment on "An Analog Photoresistive Multiplier". *IEEE Transactions on Computers*, C-17(2):188, February 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687312. See [Mas67].

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1967. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039043.

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[BBB59] Frederick P. Brooks, Jr., Gerrit A. Blaauw, and Werner Buchholz. Processing data in bits and pieces. *IRE Transactions on Electronic Computers*, EC-8 (2):118–124, June 1959. CODEN IRELAO. ISSN 0367-9950. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219512. This paper contains on page 121 the first published reference to the term "byte". An article of the same title appears in "Information Processing, Proceedings of the International Conference on Information Processing, UNESCO, Paris, 15–20 June 1959", pp. 375–381, 1959. From [Buc62, page 40]: "Byte denotes a group of bits used to encode a character, or the number of bits transmitted in parallel to and from input-output units. A term other than character is used here because a given character may be represented in different applications by more than one code, and different codes may use different numbers of bits (i.e., different byte sizes). In input-output transmission the grouping of bits may be completely arbitrary and have no relation to actual characters. (The term is coined from bite, but respelled to avoid accidental mutation to bit.)".

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[BBK+68] G. H. Barnes, R. M. Brown, M. Kato, D. J. Kuck, D. L. Slotnick, and R. A. Stokes. The ILLIAC IV computer. *IEEE Transactions on Computers*, C-17(8):746–757, Au-

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[BCE61] D. R. Bennion, H. D. Crane, and D. C. Engelbart. A bibliographical sketch of all-magnetic logic schemes. *IRE Transactions on Electronic Computers*, EC-10 (2):203–206, June 1961. CODEN IRELAO. ISSN 0367-9950. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219190.

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[BD64] J. A. Brzozowski and W. A. Davis. On the linearity of autonomous sequential machines. *IEEE Transactions on Electronic Computers*, EC-13(6):673–679, December 1964. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038298.

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[Bek65] G. A. Bekey. R65-11 methods for solving engineering problems using analog computers. *IEEE Transactions on Electronic Computers*, EC-14(1):92–93, February 1965. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038371`.

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[Bek67] G. A. Bekey. R67-30 analog computer methods for parameter optimization. *IEEE Transactions on Electronic Computers*, EC-16(2):242, April 1967. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039056`.

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[Ben60] D. R. Bennion. A note on magnetic shift registers. *IRE Transactions on Electronic Computers*, EC-9(2):262, June 1960. CODEN IRELAO. ISSN 0367-9950. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219828`.

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[Ben69a] A. Ben. R69-21 hybrid assumed mode solution of non-linear partial

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[Ben69b] R. D. Benham. R69-1 transport time-delay simulation for transmission line representation. *IEEE Transactions on Computers*, C-18(1):83, January 1969. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1671128`.

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[Ber60a] R. H. Bergman. Tunnel diode logic circuits. *IRE Transactions on Electronic Computers*, EC-9(4): 430–438, December 1960. CODEN IRELAO. ISSN 0367-9950. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219881`.

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[Ber60b] Ted W. Berwin. Frequency-to-period-to-analog computer for flowrate measurement. *IRE Transactions on Electronic Computers*, EC-9(1):62–71, March 1960. CODEN IRELAO. ISSN 0367-9950. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5221606`.

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[Ber66b] A. J. Bernstein. Computer group news. *IEEE Transactions on Electronic Computers*, EC-15(4):682, August 1966. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038850`.

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[Bey63] W. A. Beyer. Uniqueness of weighted code representations, II. *IEEE Transactions on Electronic Computers*, EC-12(2):137, April 1963. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4037816`.

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[BFL+66b] N. A. Ball, H. Q. Foster, W. H. Long, I. E. Sutherland, and R. L. Wigington. A shared memory computer display system. *IEEE Transactions on Electronic Computers*, EC-15(5):750–756, October 1966. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038882.

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[BH67] G. D. Bergland and H. W. Hale. Digital real-time spectral analysis. *IEEE Transactions on Electronic Computers*, EC-16 (2):180–185, April 1967. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039026.

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[BHK62] P. R. Bryant, F. G. Heath, and R. D. Killick. Counting with feedback shift registers by means of a jump technique. *IRE Transactions on Electronic Computers*, EC-11(2):285–286, April 1962. CODEN IRELAO. ISSN 0367-9950. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219364.

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[BHRS61] W. A. Barrett, F. B. Humphrey, J. A. Ruff, and H. L. Stadler. A card-changeable permanent-magnet-twistor memory of large capacity. *IRE Transactions on Electronic Computers*, EC-10(3): 451–461, September 1961. CODEN IRELAO. ISSN 0367-9950. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219234.

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[Bic60] Theodore A. Bickart. Computation of an expression for the frequency spectrum of a two-sided signal. *IRE Transactions on Electronic Computers*, EC-9 (2):263–264, June 1960. CODEN IRELAO. ISSN 0367-9950. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219831.

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[Bie68] B. C. Biega. R68-7 analog simulation of ferroresonant system including analysis of hysteresis loop. *IEEE Transactions on Computers*, C-17(2):192, February 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687317`.

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[Bin68] S. P. Bingulac. R68-38 accurate analog computer generation of Bessel functions for large ranges. *IEEE Transactions on Computers*, C-17(8):819, August 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687462`.

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[Bir67] L. G. Birta. Comment on "Nonobservable or Noncontrollable Analog Schemes for the Solution of a Special Class of Differential Equations". *IEEE Transactions on Electronic Computers*, EC-16(4):516–517, August 1967. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039122`. See [Mar66b].

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[Bit59] Eric E. Bittmann. Thin-film memories. *IRE Transactions on Electronic Computers*, EC-8(2):92–97, June 1959. CODEN IRELAO. ISSN 0367-9950. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219508`.

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[Bit62] E. E. Bittmann. Review: *Proposal for Demonstration of Domain Wall Storage and Logic*, by D. O. Smith. *IRE Transactions on Electronic Computers*, EC-11(3):423, June 1962. CODEN IRELAO. ISSN 0367-9950.

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[Che68a] Chi-Hau Chen. Computer processing of radiation sensor PCM data. *IEEE Transactions on Computers*, C-17(10):987–989, October 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687245`.

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[Che68b] K. A. Chen. Computer aided memory design using transmission line models. *IEEE Transactions on Computers*, C-17(7):640–648, July 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687420`.

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[Chi63] P. M. Chirlian. The fabrication of low resistances at liquid helium temperatures. *IEEE Transactions on Electronic Computers*, EC-12(2):148, April 1963. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4037820`.

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[Cho63c] C. K. Chow. Notices. *IEEE Transactions on Electronic Computers*, EC-12(6):963, December 1963. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038050.

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[Cho64] Yohan Cho. Author index. *IEEE Transactions on Electronic Computers*, EC-13(4):501, August 1964. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038223.

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[Chu66b] W. W. Chu. Correction. *IEEE Transactions on Electronic Computers*, EC-15(3):327, June 1966. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038768`.

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[Chu67a] Wesley W. Chu. A computer simulation of electrical loss and loading effect in magnetic recording. *IEEE Transactions on Electronic Computers*, EC-16(4):430–434, August 1967. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039106`.

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[Chu67b] Wesley W. Chu. A mathematical model for diagnosing system failures. *IEEE Transactions on Electronic Computers*, EC-16

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